



# Microelectronics/Semiconductor Research Community Virtual Workshop



September 8 and 9, 2022 / 12pm – 3:30pm EDT

In response to the recent efforts of the federal government to bolster US semiconductor manufacturing under the auspices of the CHIPS Act, the NSF NNCI is organizing a two-day online workshop to examine how the NNCI can address various components of the CHIPS initiative. The anticipated outcome of this workshop will be a report that could inform CHIPS funding as it relates to USICA, NSTC, NAMP and the Microelectronics Commons, as well as NSF Engines and FuSe. The workshop will combine invited talks by leading researchers in academia and industry with panel discussions to develop concrete action items to address the CHIPS initiative.

### SCHEDULE:

TIME (EDT)	DAY 1 Thursday; September 8, 2022	DAY 2 Friday; September 9, 2022
12:00pm – 1:30pm	<b>SESSION 1 R&amp;D Challenges</b> Chair: H.S. Philip Wong (Stanford)	<b>SESSION 3 Workforce Development</b> Chair: Shyam Aravamudhan (NC A&T Univ.)
	<b>Invited Speaker Presentations:</b>	<b>Invited Speaker Presentations:</b>
12:00pm	Welcome, Introduction (Sanjay Banerjee)	Welcome, Day 1 Summary (Sanjay Banerjee)
12:15pm	Jack Kavalieros (Intel Fellow)	Tsu-Jae King Liu (Dean of Eng., Berkeley)
12:45pm	Nirmal Ramaswamy (VP, Micron)	Gabriela Cruz Thomas (Director of Univ. Research, Intel)
1:15pm	Vijay Narayan (IBM Fellow)	<b>Panel Discussion:</b> Peter Bermel* (Purdue), Emmanuel Giannelis* (Cornell), Rick McCormick (Sandia)
1:45pm	<b>Break</b>	<b>Break</b>
2pm – 3:30pm	<b>SESSION 2 Materials, Supplies, &amp; Equipment</b> Chair: Trevor Thornton (ASU)	<b>SESSION 4 Academic Infrastructure</b> Chair: Sanjay Banerjee (U Texas)
	<b>Invited Speaker Presentations:</b>	<b>Invited Speaker Presentations:</b>
2:00pm	Mike Chudzik (VP, AMAT)	Oliver Brand (NNCI)
2:30pm	Victor Moroz (Synopsys Fellow)	Jesus del Alamo (MIT)
3:00pm	Prith Banerjee (Ansys CTO)	<b>Panel Discussion:</b> Raj Jammy* (Mitre), Larry Goldberg (NSF), Jim Plummer (Stanford)

(Precise times subject to change)  
Panel Discussion Chairs\*

**REGISTRATION:** [https://stanforduniversity.qualtrics.com/jfe/form/SV\\_3xUNmR8l3ECLyaa](https://stanforduniversity.qualtrics.com/jfe/form/SV_3xUNmR8l3ECLyaa)

**MORE INFORMATION:** [https://nnci.net/sites/default/files/inline-files/Microelectronics%20Workshop%202022\\_2.pdf](https://nnci.net/sites/default/files/inline-files/Microelectronics%20Workshop%202022_2.pdf)

